**SEM I**

**Program: Computer Engineering SAP ID: 60004200132**

**Basic Electrical and Electronics Engineering**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Expt. No.** | **Title of the Experiment** | **Date of Expt.** | **Date of Submission** | **CO** | **Signature** |
| 1 | DC circuit analysis | 06/03/21 | 13/03/21 | 1 |  |
| 2 | Mesh and nodal analysis | 13/03/21 | 20/03/21 | 1 |  |
| 3 | Superposition theorem | 20/03/21 | 26/03/21 | 2 |  |
| 4 | Thevenin’s and Norton’s theorem | 27/03/21 | 02/04/21 | 2 |  |
| 5 | R-L and R-C circuit analysis | 03/04/21 | 09/04/21 | 3 |  |
| 6 | Rectifier circuits | 10/04/21 | 16/04/21 | 5 |  |
| 7 | Clipper and Clampers | 17/04/21 | 23/04/21 | 5 |  |

a